## **EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L7	155	"714"/\$.ccls. and scan and (clock near3 domain) and delay	US-PGPUB; USPAT	OR	OFF	2007/06/14 21:32
L8	3676	((serial with parallel) with (conver\$5 adj circuit))	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2007/06/14 21:32
L9	4145	(scan ADJ chain\$4)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2007/06/14 21:32
L10	11	L9 and L8	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OŅ	2007/06/14 21:32
L11	480	(parallel adj input) with (serial adj output) with (shift adj register)	US-PGPUB; USPAT	OR	ON	2007/06/14 21:32
L12	12	((parallel adj input) with (serial adj output) with (shift adj register)) same select\$3 same flip	US-PGPUB; USPAT	OR	ON	2007/06/14 21:32

## **EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	0	714/726.ccls. and ((parallel adj input) with (serial adj output) with (shift adj register)) same select\$3 same flip	US-PGPUB	OR	ON	2007/06/14 21:30
L4	5	714/726.ccls. and (parallel adj input) with (serial adj output) with (shift adj register)	US-PGPUB	OR	ON	2007/06/14 21:31
L5	6	714/726.ccls. and parallel with input with serial with output with (shift adj register)	US-PGPUB	OR	ON	2007/06/14 21:31
L6	10	714/726-729.ccls. and parallel with input with serial with output with (shift adj register)	US-PGPUB	OR	ON	2007/06/14 21:32